

2017 Belle II TRG/DAQ workshop

Wednesday, 23 August 2017

Session 12: trigger 2 - Room 833 (11:00 - 12:30)

-Conveners: Y. Unno

time	[id] title	presenter
11:00	[38] ECL trg readout	S.H. KIM (HANYANG UNIV.)
11:20	[39] ECL trg slow control	C.H. KIM (HANYANG UNIV.)
11:40	[40] ECL trg DQM	W.J. CHOI (KOREA UNIV.)
11:50	[41] ECL trg simulation	I.S. LEE (HANYANG UNIV.)
12:10	[42] ECL trg calibration	Y.J. KIM (KOREA UNIV.)